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09/683091

		Subclass	ISSUE CLASSIFICATION
	Class		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <u>TR3</u> Q.A. <u>Am</u>	

APPLICATION NO. 09/683091	CONT/PRIOR	CLASS 714	SUBCLASS 371	ART UNIT 2433- 2154	EXAMINER WILSON
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William Corti
Robert Kenny
Joseph Marsh
Steven Parker

On-chip logic analyzer

TITLE

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner)	(Date)			
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	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of				ISSUE BATCH NUMBER	

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